

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	3/9/14	Introduction	2			
1	8/9/14	Induction Filter	I			
1	11/9/14	Factors for	I			
1	10/9/14	Induction filter				
1	14/9/14	C- Filter	I			
1	15/9/14	Test for 'C'	I			
1	17/9/14	Filter	I			
1	18/9/14	Comparison of	I			
2	2	Filter				
1	19/9/14	Simple Regulator	II			
1		Problems on Regulator	I			
		Weg Zener diode	-			
		Op-amp				
1	23/9/14	Operational point	II			
1	24/9/14	Basic Amplifier	II			
1	26/9/14	Fixed bias	II			
1	27/9/14	Collector bias	II			
2	28	Base bias				
4	29/9/14	Self-bias	II			
4	8/10/14	Stabilized filter	-			
		(S^1, S^2, S^3)				

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2	8/10/14	Bias Compensation	<u>II</u>			
2	10/10/14	Thermal Runaway	<u>II</u>			
2	11/10/14	Thermal Stability	<u>II</u>			
3	12/10/14	FET as Voltage Variable Resistor	<u>II</u>			
		and biasing				
1	13/10/14	Two Port Model	<u>III</u>			
1	14/10/14	Hybrid Model	<u>III</u>			
2	14/10/14	H-parameters	<u>III</u>			
		Calculation				
⑤ 3	30/10/14	CE - Amplifier	<u>III</u>			
		Calculation				
2	2/11/14	FET Model	<u>III</u>			
4	5/11/14	Simplified CE	<u>IV</u>			
		hybrid model				
1	9/11/14	CB hybrid model	<u>IV</u>			
② 1	16/11/14	CE with 'Pc'	<u>IV</u>			
2	1	Miller Theorem				

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1	12/12/16	Common Source (FET)	IV			
2	15/12/16	Common drain (FET)	IV			
2	16/12/16	h _{ie} Model for CE	IV			
1	18/12/16	Hybrid Conductance	V			
1	23/12/16	Hybrid Capacitance	V			
1	24/12/16	Validity of hybrid Model	V			
1	24/12/16	CE 'SC' - gain	V			
1	29/12/16	CE unit-load	V			
3	30/12/16	CE Amplifier	V			
		DC Power				
1	31/12/16	EC - Amplifier	V			
2		CS FET				
2		CD FET				