

(1)	01/9/14	Introduction	1	BB		
(2)	02/9/14	Number System Conversion, from Decimal to Other base, Other	1	BB		
(3)	02/9/14	base to decimal practice problem	1	BB		
(4)	03/9/14	Conversion from any base to any base sample problems.	1	BB		
(5)	06/9/14	problem Solving	1	BB		
(6)	08/9/14	problem Solving	1	BB		
(7)	09/9/14	Complements of a number.	1	BB		
		Example, problem Solving				
(8)	10/9/14	Subtraction using complements	1	BB		
		7's & 9's comp				
		problem Solving				
(9)	15/9/14	problem Solving	1	BB		
(10)	16/9/14	problem Solving	1	BB		
(11)	16/9/14	Codes Gray, BCD, BCD addition, $6 \rightarrow B$ & $B \rightarrow 6$ procedure, problem Solving.	1	BB		
(12)	17/9/14	8421, 8421 codes, XS-3 code	1	BB		
		XS-3 addition, problem Solving.	1	BB		
(13)	20/9/14	Parity code, Hamming code, error Correction & detection.				

# LESSON PLAN

Period	Date (tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
(14)	23/9/14	Introduction to Logic gates	1	BB		
(15)	23/9/14	Boolean Expression construction + few	2	BB		
(16)	24/9/14	Boolean theorems, minimization of Switching function	2	BB		
(17)	6/10/14	Minimization problems,	2	BB		
(18)	7/10/14	Minimization problems	2	BB		
(19)	7/10/14	obtaining dual complement	2	BB		
(20)	8/10/14	problems on complementing NAND-NOR implementation	2	BB		
(21)	17/10/14	Multilevel NAND NOR realization	2	BB		
(22)	25/10/14	problem on NAND-NOR realization	2	BB		
(23)	26/10/14	SOP & POS concepts conversion	2	BB		
(24)	29/10/14	concept of truth tables and writing equivalent form from truth tables & vice versa	2	BB		
(25)	3/11/14	Introduction to K-map & V map	2	BB		
(26)	4/11/14	3,4-variable problem solving	2	BB		
(27)	4/11/14	5-variable problem solving	2	BB		
(28)	5/11/14	problem solving	2	BB		
(29)	10/11/14	Tabular Method	2	BB		

	Date	Topic	No	Methodology		Upon Review
(31)	11/11/14	Combinational using K-map	2	BB		
(32)	12/11/14	Combinational design, procedure	3	BB		
(33)	12/11/14	Design of half, full adder, half & full adders parallel adder	8	BB		
(34)	15/11/14	Parallel subtractor 4046 circuit	8	BB		
(35)	18/11/14	Carry look ahead adder	8	BB		
(36)	19/11/14	BCD adder, XS-3 adder, operation	3	BB		
(37)	20/11/14	design of decoder Examples	8	BB		
(38)	24/11/14	4 <sup>th</sup> realization using decoder	8	BB		
(39)	25/11/14	4 <sup>th</sup> realization using decoder	8	BB		
(40)	26/11/14	4 <sup>th</sup> realization using decoder	8	BB		
(41)	1/12/14	4 <sup>th</sup> realization using mux	8	BB		
(42)	2/12/14	problem on decoder & mux	3	BB		
(43)	3/12/14	4 bit priority encoders	8	BB		
(44)	3/12/14	demux, 4 <sup>th</sup> realization, example	8	BB		
(45)	8/12/14	Comparator	8	BB		
(46)	9/12/14	8000 segment display	8	BB		
(47)	15/12/14	2 <sup>nd</sup> order input sequential CAB	4	BB		
(48)	16/12/14	RS latch, analysis truth table, char. table, char. equation	4	BB		

# LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
(49)	16/12/14	J K latch Characteristic equation	4 BB	Y BB		
(50)	17/12/14	table D-latch, T-latch, Toggling flip flops, RS, JK, D flip	BB	Y BB		
(51)	22/12/14	flip flop conversion excitation Design of	4 BB	Y BB		
(52)	23/12/14	ripple counters	4 BB	Y BB		
(53)	23/12/14	Synchronous Johnson counter Shift reg. design control buffer Reg. universal Shift reg.	4 BB	Y BB		
(54)	24/12/14	problem solving	4 BB	Y BB		
(55)	29/12/14	problem on counters and Shift reg. Serial adder	BB	Y BB		
(56)	30/12/14	Design of counters using various flip flops	4 BB	Y BB		
(57)	30/12/14	problem on using flip flops (GATE)	BB			
(58)	3/1/15	Introduction to VHDL programming program. First 5	5	BB		
(59)	5/1/15	Defining, Logic gates, writing program for	5	BB		