

(A.Y - 2014-15) I Sem

LICA

## LESSON PLAN

Period	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
		Unit - I:				
1	25.08.14	Introduction to LICA	I	CR		
2	26.08.14	Analysis of dual input balanced output (DC)	I	"		
3	27.08.14	AC Analysis of diff amp	I	"		
4	27.08.14	Analysis of dual input unbalanced output diff amp	I	"		
5	28.08.14	Analysis of single ended balanced output diff amp	I	"		
6	1.09.14	Analysis of single ended 1/2 unbalanced diff amp	I	"		
7	2.09.14	DC coupling & cascade diff amplifier stages	I	"		
8	3.09.14	Level translator	I	"		
		Unit - II:				
9	5.09.14	Block diagram of op-amp	II	CR		
10	6.09.14	Types of IC op-amp classification	II	"		
		package types & temp parameters, ranges	II	"		
11	8.09.14	Ideal op-amp characteristics and specification	II	"		
12	9.09.14	DC Characteristics	II	"		
13/14	10.09.14 10.09.14	AC characteristics	II	"		
15	15.9.14	741 op-amp & its features	II	"		
16	16.9.14	op-amp parameters & measurements	II	"		
17, 18	17.9.14	Input & output offset voltages & currents, CMRR	II	"		
		Slew rate, rise, fall time	II	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		<u>Unit - III:</u>				
19	18.9.14	Inverting & non-inverting op-amp	<u>III</u>	CR		
20	19.9.14	Integrator	<u>III</u>	"		
21	22.9.14	Differentiator, diff amp	<u>III</u>	"		
22	23.9.14	Instrumentation amplifier	<u>III</u>	"		
23	24.9.14	AC amplifier, Buffer	<u>III</u>	"		
25	25.9.14	V to I converter	<u>III</u>	"		
26	29.9.14	I to V converter	<u>III</u>	"		
		<u>Unit - IV:</u>				
27	30.10.14	Monobus function generator	<u>IV</u>	CR		
28	6.10.14	Comparator	<u>IV</u>	"		
29	7.10.14	Multivibrator	<u>IV</u>	"		
30	8.10.14	Triangular wave generator	<u>IV</u>	"		
31	8.10.14	Square wave generator	<u>IV</u>	"		
32	9.10.14	Logarithmic amplifier	<u>IV</u>	"		
33	10.10.14	Anti log amplifier	<u>IV</u>	"		
		precision rectifier	<u>IV</u>	"		
		<u>Unit - V:</u>				
34	20.10.14	Introduction to filters 1st order LPF	<u>V</u>	CR		
		1st order HPF	<u>V</u>	"		

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35	21.10.14	2nd order LCR & RLC	V	CR		
36	21.10.14	Band pass filter	V	"		
37	22.10.14	Band reject filter	V	"		
38	27.10.14	All pass filter	V	"		
Unit - VI:						
39	21.10.14	SSR two in diagram	VI	CR		
40	29.10.14	Monolithic mult using SSR	VI	"		
41	29.10.14	Applications	VI	"		
42	30.10.14	Antic mult using SSR	VI	"		
43	3.11.14	Applications	VI	"		
44	4.11.14	Schmitt trigger	VI	"		
45	5.11.14	Principle & description of PLL	VI	"		
46	5.11.14	SSR PLL	VI	"		
47	6.11.14	Applications of PLL - frequency multiplication	VI	"		
48	10.11.14	AM, FM demodulation	VI	"		
49	11.11.14	Application of VCO (VCC)	VI	"		
Unit - VII:						
50	12.11.14	Weighted resistor DAC	VII	CR		
51	12.11.14	R-2R ladder, Inverted R-2R DAC	VII	CR		
52	13.11.14	IC 1408 DAC	VII	"		

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