

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
58	24-11-14 21-11-14	Non uniform memory Access	VII	CR		
59 60	27-11-14 26-11-14	Vector computation	VII	CR		
61 62	26-11-14 27-11-14	Characteristics of multiprocessor	VIII	CR		
63 64	1-12-14 3-12-14	Interconnection Structure	VIII	CR		
65 66	4-12-14 5-12-14	Interprocessor arbitration	VIII	CR		
67 68	8-12-14 10-12-14	Inter process communication & synchronization	VIII	CR		

	Sl. No.	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
30	9-10-19	floating point	IV	CR		
31	13-10-19	arithmetic operator				
32	16-10-19	Decimal arithmetic	IV	CR		
33	16-10-19	Unit				
34	18-10-19	Decimal arithmetic	IV	CR		
35	18-10-19	operation				
36	20-10-19	memory hierarchy	V	CR		
36	22-10-19	Associative memory	V	CR		
37	22-10-19					
38	24-10-19	cache memory	V	CR		
37	23-10-19					
40	29-10-19	Virtual memory	V	CR		
41	29-10-19		V	CR		
42	30-10-19	memory mapped hardware	V	CR		
43	31-10-19	Peripheral device	VI	CR		
44	3-11-19	Input-output interface	VI	CR		
45	5-11-19	Asynchronous	VI	CR		
46	5-11-19	data transfer rate of transfer				
47	6-11-19	Direct memory access	VI	CR		
48	7-11-19	Input-output processor	VI	CR		
49	10-11-19	serial communication	VI	CR		
50	12-11-19					
51	12-11-19	Introduction to parallel processing	VII	CR		
52	13-11-19	Use of multiprocessor	VII	CR		
53	14-11-19	Symmetric multi	VII	CR		
54	15-11-19	processor				
55	18-11-19	Cache coherence problem	VII	CR		
56	18-11-19	MESI Protocol	VII			

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1	25-08-19	Introduction to COA	I	CR	
2	27-08-19	Computer components & computer function	I	CR	
3 4	28-08-19 29-08-19	Bus & data connection	I	CR	
5	1-9-19	Integer representation	I	CR	
6	3-9-19	Fixed point representation	I	CR	
7 8	3-9-19 4-9-19	Floating point representation	I	CR	
9	5-9-19	machine instruction characteristics	II	CR	
10 9	8-9-19	Assembly Language	II	CR	
11	10-9-19	Addressing mode	II	CR	
12	10-9-19	Instruction format	II	CR	
13 14	11-9-19 12-9-19	processor register organization	II	CR	
15	15-9-19	Instruction cycle	II	CR	
16 17	17-9-19 17-9-19	Instruction Pipeline	II	CR	
18	18-9-19	microoperation & control of processor	III	CR	
19 20	19-9-19 22-9-19	Hardwired control	III	CR	
21 22	24-9-19 24-9-19	microprogrammed control	III	CR	
23	25-9-19	micro instruction set	III	CR	