

LESSON PLAN

B.B. Black Board

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Class Action / Review
1	25.8.14	Introduction to logic circuits. Difference b/w Analog & Digital Circuits	I	B.B.		
2	25.8.14	Different types of logic families & their characteristics	"	"		
3	26.8.14	MOS Transistors BIAS & types of MOSFETs.	"	"		
4	27.8.14	CMOS logic & examples	"	"		
5	28.8.14	Design of logic gates by CMOS logic.	"	"		
6	1.9.14	Steady state Electrical behaviour of CMOS	"	"		
7	1.9.14	Dynamic Electrical behaviour of CMOS.	"	"		
8	2.9.14	CMOS logic families.	"	"		
9	3.9.14	Bipolar logic & Interfacing.	II	"		
10	4.9.14	Diode logic, Bipolar, transistor logic.	"	"		
11	8.9.14	TTL families.	"	"		
12	8.9.14	CMOS/TTL Interfacing	"	"		
13	9.9.14	Low voltage CMOS logic & Interfacing.	"	"		
14	10.9.14	Emitter Coupled logic & specifications.	"	"		
15	11.9.14	Comparisons of logic families.	"	"		
16	15.9.14	familiarity with 74xx & CMOS 40xx ICs.	"	"		
17	15.9.14	Introduction to Combinational logic design.	III	"		
18	16.9.14	Design of Decoder, Encoder & Three state devices	"	"		
19	17.9.14	Mux & Demux design modules	"	"		
20	18.9.14	Design of Code Converter Converters.	"	"		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
21	22.9.14	Ex-OR gates & parity Circuits design.	"	Black Board		
22	22.9.14	Comparators, adders & subtractors.	"	"		
23	23.9.14	Design Considerations of the above	"	"		
24	24.9.14	Combinational logic Ckts with relevant Dcs.	"	"		
25	25.9.14	Introduction to Combinational Ckts.	<u>IV</u>	"		
26	6.10.14	Design of Ripple Adder.	"	"		
27	6.10.14	Look ahead Carry Generator.	"	"		
28	7.10.14	Binary parallel Adder.	"	"		
29	8.10.14	n-bit parallel Subtracter.	"	"		
30	9.10.14	Binary Adder-Subtractor & ALU's	"	"		
31	13.10.14	Combinational multipliers & Barrel Shifter.	"	"		
32	13.10.14	Simple floating point Encoder.	"	"		
33	14.10.14	Cascading Comparator & Dual priority Encoder.	"	"		
34	15.10.14	Design Considerations of the above Circuits.	"	"		
35	16.10.14	Introduction to Sequential logic Circuits.	<u>V</u>	"		
36	20.10.14	The basic Bistable Element.	"	"		
37	20.10.14	Latches & Flip-flops	"	"		
38	21.10.14	Flip-flop Conversions	"	"		
39	22.10.14	SSI Latches and flip-flops.	"	"		
40	22.10.14	Introduction to Counters.	"	"		

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Period	Date Tentative	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
41	27/10/14	Design of Counters using Digital ICs Applications	"	Black board		
42	28/10/14	Synchronous Design Methodology	"	"		
43	29/10/14	Impediments to Synchronous design	"	"		
44	30/10/14	Design Considerations of Sequential Circuits	"	"		
45	31/11/14	MSI registers, Shift registers	<u>VI</u>	"		
46	3/11/14	Modes of operation of shift registers	"	"		
47	5/11/14	Universal shift registers, MSI shift registers	"	"		
48	6/11/14	Ring Counters & Johnson Counters	"	"		
49	10/11/14	Basic logic design (Sequential) Steps	"	"		
50	10/11/14	Design of Modulus N Synchronous Counter	"	"		
51	11/11/14	Design Considerations of the above	"	"		
52	12/11/14	Relevant ICs with relevant digital ICs	"	"		
53	13/11/14	Introduction to Programmable logic design	<u>VII</u>	"		
54	17/11/14	Programmable read only memory	"	"		
55	17/11/14	Programmable logic Array (PLA)	"	PPT		
56	18/11/14	Programmable Array logic devices	"	"		
57	19/11/14	Comparison between PROM, PLA & PAL	"	"		
58	20/11/14	Design Considerations of PLD	"	R-B		
59	20/11/14	ICs with relevant digital ICs	"	"		
60	24/11/14	Other design Examples of PLD	"	PPT		

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Period	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
61.	25-11-14	Internal structure of Memories.	<u>VIII</u>	BB		
62.	26-11-14	2D- Decoding & Commercial Rom types.	"	"		
63.	27-11-14	Timing & application of Rom types.	"	"		
64.	1-12-14	Internal structure of Static RAM.	"	"		
65.	1-12-14	SRAM Timing. Standard SRAMs Synchronous SRAMs.	"	"		
66.	2-12-14	Internal structure of DRAM, timing.	"	"		
67.	3-12-14	Synchronous DRAM. Functionally with	"	"		
68.	4-12-14	Component data sheets CY- 6116, CY-AC1006 specifications.	"	"		

Date
12/12/14