**LESSON PLAN**

Branch/ Semister/Session : II ECE (A) (1st semester)

Academic Year : 2015 – 2016

Subject Name : Electronic Circuits – 1(EC1)

Faculty Name : Dr. A. S. SRINIVASA RAO

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| Period | Date(tentative) | Topic | Unit no | Teaching methodology |
| 1 | 05-08-15 | Introduction to Harmonic components | I | Class room teaching |
| 2 | 05-08-15 | Capacitive filter |
| 3 | 06-08-15 | Inductive filter |
| 4 | 10-08-15 | LC filter |
| 5 | 12-08-15 | Π filter |
| 6 | 12-08-15 | Problems |
| 7 | 13-08-15 | Problems |
| 8 | 17-08-15 | Zener diode as regulator |
| 9 | 19-08-15 | Zener diode as regulator |
| 10 | 19-08-15 | Problems |
| 11 | 20-08-15 | Operating point, Need of biasing | II | Class room teaching |
| 12 | 24-08-15 | Stability factors & fixed bias methods (BJT) |
| 13 | 26-08-15 | Self bias & collector to base bias (BJT) |
| 14 | 26-08-15 | Voltage divider bias (BJT), Thermal runaway |
| 15 | 27-08-15 | Compensation techniques |
| 16 | 31-08-15 | Thermal resistance & thermal stability |
| 17 | 02-09-15 | FET biasing |
| 18 | 02-09-15 | MOSFET biasing |
| 19 | 03-09-15 | Problems |
| 20 | 07-09-15 | Problems |
| 21 | 14-09-15 | Problems | III | Class room teaching |
| 22 | 16-09-15 | Problems |
| 23 | 16-09-15 | Problems |
| 24 | 17-09-15 | Problems |
| 25 | 21-09-15 | Two port network representation |
| 26 | 23-09-15 | h-parameter model |
| 27 | 23-09-15 | CB configuration equivalent |
| 28 | 24-09-15 | CE & CC configuration equivalent |
| 29 | 28-09-15 | Equivalence between CB, CE and CC configuration |
| 30 | 30-09-15 | Measurement of h-parameters |
| 31 | 30-09-15 | Analysis transistor amplifier using h-parameter model |
| 32 | 01-10-15 |
| 33 | 05-10-15 | Problems |
| 34 | 07-10-15 | FET low frequency model |
| 35 | 07-10-15 | h-parameter analysis of CE amplifier | IV | Class room teaching |
| 36 | 08-10-15 | h-parameter analysis of CE amplifier | IV | Class room teaching |
| 37 | 12-10-15 | h-parameter analysis of emitter follower |
| 38 | 14-10-15 | h-parameter analysis of CB amplifer |
| 39 | 14-10-15 | Miller theorem and dual |
| 40 | 19-10-15 | FET CS amplifier analysis |
| 41 | 21-10-15 | FET CD amplifier analysis |
| 42 | 21-10-15 | FET CG amplifier analysis |
| 43 | 22-10-15 | Problems |
| 44 | 26-10-15 | Problems |
| 45 | 28-10-15 | Problems |
| 46 | 28-10-15 | Problems |
| 47 | 29-10-15 | Transistor HF model & parameter explanation | V | Class room teaching |
| 48 | 02-11-15 | Hybrid conductance parameters |
| 49 | 04-11-15 | Hybrid capacitance parameters |
| 50 | 04-11-15 | Variations in hybrid parameters |
| 51 | 05-11-15 | CE short circuit current gain |
| 52 | 09-11-15 | CE current gain with resistive load |
| 53 | 11-11-15 | CE amplifier response |
| 54 | 11-11-15 | Gain bandwidth product |
| 55 | 12-11-15 | Emitter follower at high frequency |
| 56 | 16-11-15 | FET at high frequency |
| 57 | 18-11-15 | Revision |
| 58 | 18-11-15 | Revision |
| 59 | 19-11-15 | Revision |
| 60 | 23-11-15 | Revision |