

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
1	5.8.2015	<u>Filters and Regulation I</u>		BB		
2	6.8.2015	Harmonic compo-		BB		
3	7.8.2015	nents 2 nd in a recti-		BB		
		fier circuit, conductor		BB		
4	7.8.2015	filter, capacitor		BB		
5	12.8.2015	filter, I - section		BB		
		filter, II - section		BB		
		filter, multiple		BB		
6	13.8.2015	I - section and II -		BB		
		section filters		BB		
7	14.8.2015	Comparison of fil-		BB		
		ter components / Ceb		BB		
8	14.8.2015	Simple regulator		BB		
		Circuits using		BB		
9	19.8.2015	Zener diode		BB		
10	20.8.2015	<u>Transistor Reg-</u>	II	BB		
		<u>ulation and Stabiliz-</u>		BB		
11	21.8.2015	tion: operating		BB		
		point, load sta-		BB		
12	21.8.2015	bility, collector to		BB		

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13	26.8.2015	base bias, self		BB		
		bias amplifiers,		BB		
14	27.8.2015	Stabilization against		BB		
		variations in		BB		
		VBE, and β for		BB		
15	28.8.2015	Self Bias Act,		BB		
		Stabilization factors		BB		
16	28.8.2015	S, S', S'', R_{th}		BB		
		Compensation, the-		BB		
17	2.9.2015	Voltage divider and se-		BB		
		mentors compensa-		BB		
18	3.9.2015	tion, Compensa-		BB		
		tion against varia-		BB		
		tion in VBE, β and		BB		
19	4.9.2015	Thermal runaway		BB		
		and thermal		BB		
20	4.9.2015	Stability		BB		
		FEF as voltage		BB		
21	16.9.2015	Voltage divider		BB		
		and Biasing		BB		

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22	17.9.2015	Low freq. Amplifier	11	BB		
		Get rid of feedback		BB		
23	18.9.2015	Two port devices		BB		
		and Hybrid Model		BB		
24	15.9.2015	Transistor Hybrid		BB		
25	23.9.2015	Model, Determination of H		BB		
		parameters from characteristics		BB		
26	24.9.2015	Measurement of H parameters		BB		
27	25.9.2015	Conversion from one set to		BB		
28	25.9.2015	parameters of three transistor		BB		
29	30.9.2015	Comparison of		BB		
30	1.10.2015	Amplifier Circuits		BB		
		using H parameters		BB		
31	2.10.2015	Comparison of		BB		
32	2.10.2015	parameters, Comparison		BB		

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33	7.10.2015	of Transistor Amplifier		BB		
		Configurations		BB		
34	8.10.2015	FET Small Signal		BB		
35	9.10.2015	Model and analysis		BB		
		G/S -		BB		
36	9.10.2015	Single Stage	14	BB		
		Amplifiers' Sizing		BB		
37	14.10.2015	plified CE Hybrid		BB		
38	21.10.2015	Model, Synchronization		BB		
39	22.10.2015	Calculations for		BB		
40	23.10.2015	CE configurations		BB		
41	23.10.2015	and CB Amplifier		BB		
42	28.10.2015	CE Amplifier with		BB		
43	29.10.2015	Emitter resistor		BB		
44	30.10.2015	Stage, Amplifier		BB		
45	30.10.2015	follower, Miller		BB		
46	1.11.2015	theorem, and		BB		
47	5.11.2015	Dual of Miller		BB		
48	6.11.2015	theorem.		BB		
49	6.11.2015	FET CS & CD Amplifiers		BB		

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50	11.11.2015	High frequency	2	BB		
		Amplifiers: Hybrid		BB		
51	12.11.2015	CE transistors		BB		
		parallel, Hybrid		BB		
52	13.11.2015	AC conductance		BB		
53	13.11.2015	Hybrid AC capacitance, Validity		BB		
54	18.11.2015	AC Hybrid AC Model, Variations		BB		
55	18.11.2015	AC Hybrid parameters, CE stage		BB		
56	19.11.2015	Current gain, current gain with		BB		
57	20.11.2015	reactive load, Single stage		BB		
		CE transistors		BB		
58	20.11.2015	Amplifier response, Lower band		BB		
		with feedback		BB		
59	20.11.2015	Emitter follower		BB		

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